

IF YOU WANT TO LEARN MORE ABOUT DIGITAL DESIGN BASED ON FPGA.

ENROLL THIS SUMMER COURSE!

ECTS credits: 4.0











BRIEF DESCRIPTION

The course is focused on FPGA-based digital design topics.

After the course, students are expected to be able to use VHDL and Xilinx Vivado software for digital design. The course provides basic competencies in VHDLdigital design with based practical experience in simulation and hardware debugging. The course could be a good starting point for the students, going to design and computer work in IC engineering.

The course implies that students are familiar with basics of electronics and informatics. At least some knowledge of MOS transistors and binary mathematics is expected.

Our friendly team, lectures, mentors and coaches will lead the participants throughout the process, thus they can take a maximum of it.

Cost:

US\$900 - includes registration, teaching costs, 4.0 ECTS credits Certificate, additional Russian Language on-line course by ALAR Training Center (33h) + Certificate











Courses:

- Introduction to digital design
- Basics of VHSIC Hardware Description Language - Synthesis of combinational and sequential devices
- Digital design with FPGAs
- IP-cores: Creating and using

Program dates: 18 Jul - 01 Aug, 2023

Registration deadline: March 28, 2023

FORMAT: ON-CAMPUS

- Studies
- Excursions
- Support 24/7
- Certificate
- Additional program (visit to Moscow and Saint Petersburg)

